

**Abstract**

In a power source for an electric arc welder including an inverter having two primary circuits connected in a series circuit across the DC bus of an input rectifier where each primary circuit comprises a capacitor parallel with a primary winding section and a switch to apply the voltage of the capacitor across the primary winding section, so that the primary circuits alternately create a voltage pulse in the primary winding sections to induce voltage pulses in a secondary circuit having a secondary winding coupled to said primary windings and connected by an output circuit to an output welding circuit. There is provided one improvement. The improvement is a soft ferrite saturable reactor in at least one of the series circuit or the output circuit to delay the voltage pulse in the primary winding sections.